Paweł Gepner – EMEA Platform Architecture Specialist; Intel Corporation

Paweł Gepner is Platform Architecture Specialist for Europe Middle East & Africa Operation focused on High Performance Computing.

Paweł Gepner in his role as Platform Architecture Specialist is to ensure customers of server and HPC products receive world-class technical service, support, technology training and other marketing services.

Paweł Gepner has joined Intel in 1996 as Field Application Engineer for Central and Eastern Europe. In 1999 he has been appointed to a current position.

In 2001, Paweł Gepner became EMEA Architecture Specialist focused on Server and HPC area. He led couple of server development projects including first Fault Tolerance Systems based on IA-32 from Stratus Technology. He was responsible for driving Pentium III server project at IBM R&D Center in Greenock. He also led the team that developed Bull's Itanium 2 system . He was also involved in Itanium 2 projects at Siemens AG and Eriksson.

Paweł Gepner led the development team for the first teraflop computing projects in Poland and first itanium 2 teraflop installations in EMEA. He was driving many of the HPC projects in EMEA including TASK, SKODA, VW, CERN, Airbus and many others.

In addition to the Platform Architecture Specialist role he is also Intel Corporation spoke person responsible for communication with the press reg. technical and technology aspect of Intel's products and technology.

Paweł Gepner is graduated in Computer Science and he holds master's and Ph.D. degrees from Warsaw Technical University, Poland

Paweł Gepner has written 25 technical papers on Computer Science and technology. He is also a board member and technology advisor for many international scientific and commercial HPC projects.